

Department of Electronics

Following shall be the schedule of Internal Assessment for BSc (H) Electronics and B. Tech Electronics students.

Sem .	Paper	Teacher	Test 1	Test 2	Test 3
			Date	Date	Date
I	Network Analysis	Amandeep Kaur	10/9/16	1/10/16	22/10/16
	Syllabus		Unit 1-2	Unit 2-3	Unit 3-4
	Mathematics	P Arun	2/9/16	7/10/16	28/10/16
III	Electronic Circuits	Amandeep Kaur	2/9/16	30/9/16	21/10/16
	Syllabus		Unit 1	Unit 2	Unit 3
	Digital Electronics	Inderpreet Singh	30/8/16	26/9/16	24/10/16
	Syllabus		Unit 1 & Unit 2 (first half)	Unit 2 (second half) & Unit 3	Unit 4 (Verilog)
	C Programming	Davinder Kaur	15/9/16	20/10/16	5/11/16
Syllabus		Unit1	Unit 2	Unit 3	
V	Analog Comm.	Poonam Bedi	13/9/16	28/9/16	21/10/16
	Syllabus		Unit 2	Unit 3	Unit 4 (first half)
	Wave Propagation	Davinder Kaur	5/9/16	1/10/16	5/11/16
	Syllabus		Unit 1	Unit 2	Unit 3
	Microprocessors	Inderpreet Singh	7/9/16	7/10/16	3/11/16
	Syllabus		Unit 1	Unit 2	Unit 3
	Instrumentation	Nidhi Sinha	5/9/16	3/10/16	28/10/16
Syllabus		Unit 1	Unit 2 & Unit 3	Unit 3 & Unit 4	
VII	Power Electronics	Amrita Chakraborty	19/9/16	7/10/16	21/10/16
	Syllabus		Unit 1 & Unit 2 (first half)	Unit 2 (2nd half) & Unit 3	Unit 4
	Photonics	Nidhi Sinha	15/9/16	6/10/16	28/10/16
	Syllabus		Unit 1	Unit2	Unit 3
	Computer Networks	Navjot Kaur	22/9/16	25/10/16	